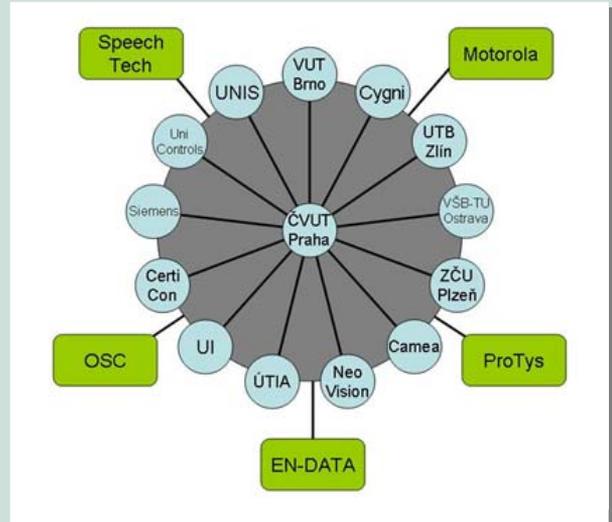


Motivation

- ▶ Czech national network for applied research
- ▶ Partners from academia and industry
- ▶ 54 persons of permanent staff (av. age 29 years)
- ▶ Funding for 2000-2004: 4.7 Meuro
- ▶ Funding for 2005-2009: 8.7 Meuro
- ▶ Universities & Academy 100% funded
- ▶ SMEs 50% funded
- ▶ End users contribute to CAK expenses

Prof. Vladimír Kučera - Head
 Prof. Michael Šebek - Manager
 4 main departments

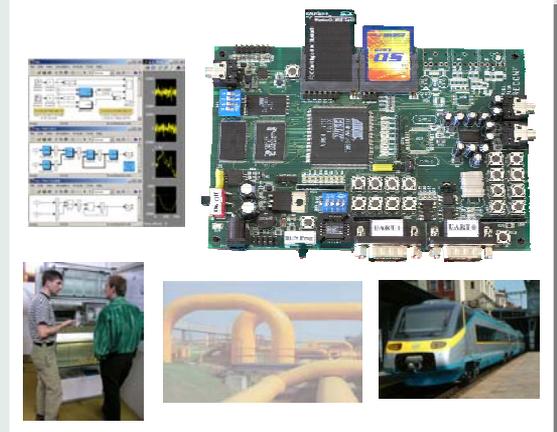
Partners



Embedded Systems Department Goals

- ▶ Signal processing in programmable circuits
 signal processing algorithms, programmable HW, dynamically re-configurable circuits, software tools, specialized libraries of functional modules
- ▶ Verification of discrete event systems
 Automata, Petri nets and process algebras, real-time models, probabilistic models
- ▶ Distributed Control Systems Design
 industrial communications, cars, trains, process control, production line automation

Doc. Zdeněk Hanzálek - Head of Department



Time frame

Started on January 1st, 2005 with a duration of 60 months.



UTIA, Dept. of Signal Processing
 Pod Vodárenskou věží 4
 182 08 Praha 8, CZE
<http://www.utia.cas.cz/ZS>



Czech Technical University in Prague
 Faculty of Electrical Engineering
 Technická 2, Praha 6, CZ
<http://www.feld.cvut.cz>



FEE CTU in Prague
 Dept. of Control Engineering
 Karlovo nám. 13, Praha 2, CZE
<http://dce.felk.cvut.cz>